
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|------------------|--------------------------|---|-----------------|------------------|
| Application No.: | 10/789,311 | § | Examiner: | Johnson, Carlton |
| Filed: | February 27, 2004 | § | Group/Art Unit: | 2436 |
| Inventors: | | § | Atty. Dkt. No: | 6000-31500 |
| | Hans Eberle | § | | |
| | Nils Gura | § | | |
| | Lawrence Spracklen | § | | |
| | Shuecling Chang-Shantz | § | | |
| | Leonard Rarick | § | | |
| Title: | Method and Apparatus for | § | | |
| | Implementing Processor | § | | |
| | Instructions for | § | | |
| | Accelerating Public-Key | § | | |
| | Cryptography | § | | |

RESPONSE TO OFFICE ACTION OF SEPTEMBER 27, 2011

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of September 27, 2011, to further highlight why the application is in condition for allowance.